Patent Claims

1. An integrated circuit arrangement which has:
an output circuit having a first output connection and a second output connection which can provide data signals,
a first data output connection and a second data output connection,
where the first output connection and the first data output connection have at least one first inductance connected between them, the first inductance being in a form such that it forms a first frequency filter having a prescribed frequency band together with the first data output connection,
where the second output connection and the second data output connection have at least one second inductance connected between them which is coupled to the first inductance, the second inductance being in a form such that it forms a second frequency filter having the prescribed frequency band together with the second data output connection.

2. The integrated circuit arrangement as claimed in claim 1, where the prescribed frequency band is in the range from 1 GHz to 100 GHz.

3. The integrated circuit arrangement as claimed in claim 1 or 2, which has a plurality of frequency filters coupled in series between the at least first output connection and the at least first data output connection.

4. The integrated circuit arrangement as claimed in one of claims 1 to 3,
where at least one of the inductances is a monolithically integrated inductance.

5. The integrated circuit arrangement as claimed in one of claims 1 to 4, where the output circuit has a differential amplifier.

6. The integrated circuit arrangement as claimed in one of claims 1 to 5, where the output circuit has a multiplexer.